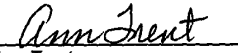


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Zhiqiang Wu, et al.	Docket No:	TI-29922.1
Serial No:	Not Assigned	Conf. No:	Not Assigned
Examiner:	TBD	Art Unit:	TBD
Filed:	07/17/2003		
For:	TRANSISTOR CIRCUIT WITH VARYING RESISTANCE LIGHTLY DOPED DIFFUSED REGIONS FOR ELECTROSTATIC DISCHARGE ("ESD") PROTECTION		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage for "Express Mail Post Office to Addressee" service under 37 CFR 1.10 and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 7-17-03.

  
Ann Trent

**PRELIMINARY AMENDMENT**

Dear Sir:

Prior to the examination of the above identified application, Applicants respectfully submit the following amendments and remarks.

**IN THE SPECIFICATION**

Please amend the specification by inserting before the first line the sentence:

--This application is a divisional of application Serial No. 10/020,687, filed December 14, 2001.--